

Amendments to the Specification:

Please replace paragraph [7] with the following amended paragraph:

[7] The two apparatuses (clock deciding apparatuses A and B) having a dual structure, as described above, perform the same operation. However, these apparatuses are in a complementary relationship with each other. If the clock signal of the apparatus A is abnormal, apparatus A receives the clock signal of apparatus B through a path connected to apparatus B and outputs it. Therefore, it is able to provide the system with a stable and continuous clock signal.

Please replace paragraph [34] with the following amended paragraph:

[34] Figure 5 is a diagram illustrating a method for selecting the delay clock of the phase comparing unit according to an embodiment of the present invention. The phase comparing unit 220 selects the delay clock signal among the delay clock signals having a same phase as that of the outer clock signal. More precisely, the phase comparing unit 220 selects the delay clock signal having a minimum phase difference from that of the outer clock signal. In Figure 5, since the second delay clock signal is '1' and the third delay clock signal is '0' when a rising edge ('0' → '1') of the outer clock signal, the delay clock having the minimum phase difference from that of the outer clock signal is the second delay clock signal or the third delay clock signal.